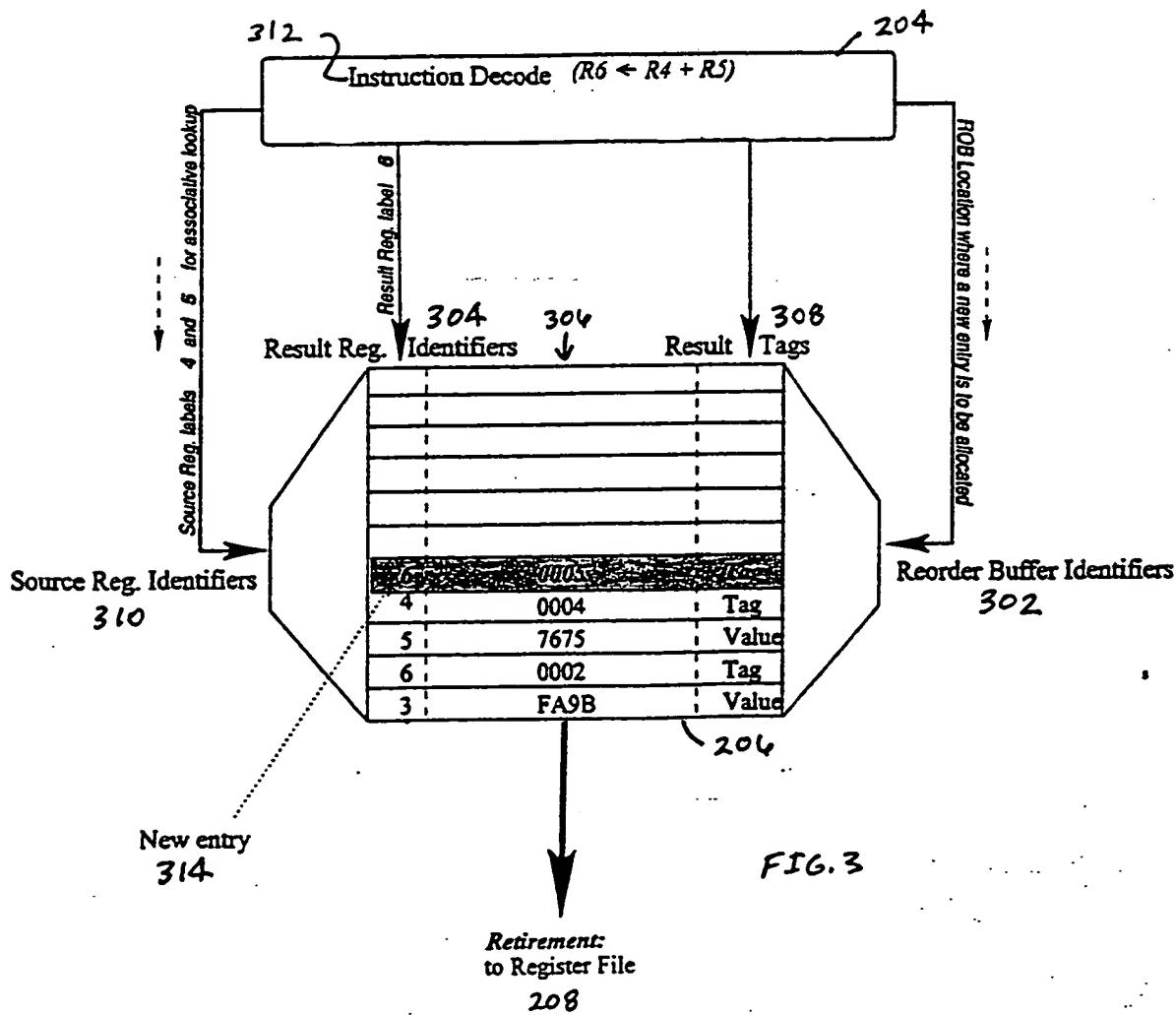


FIG. 2



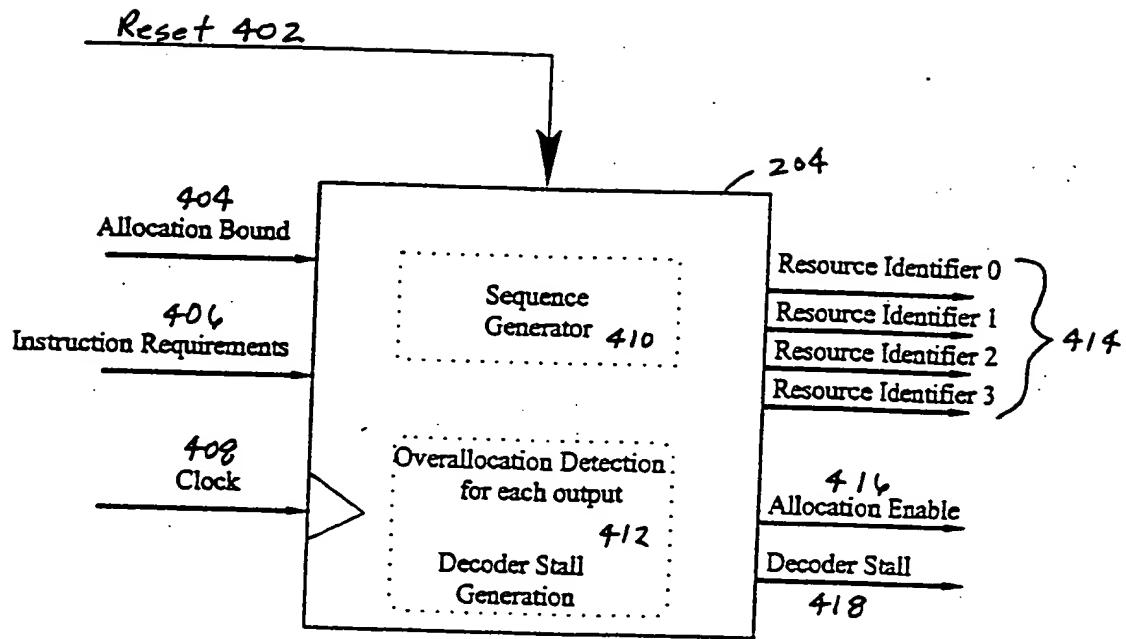


FIG. 4a

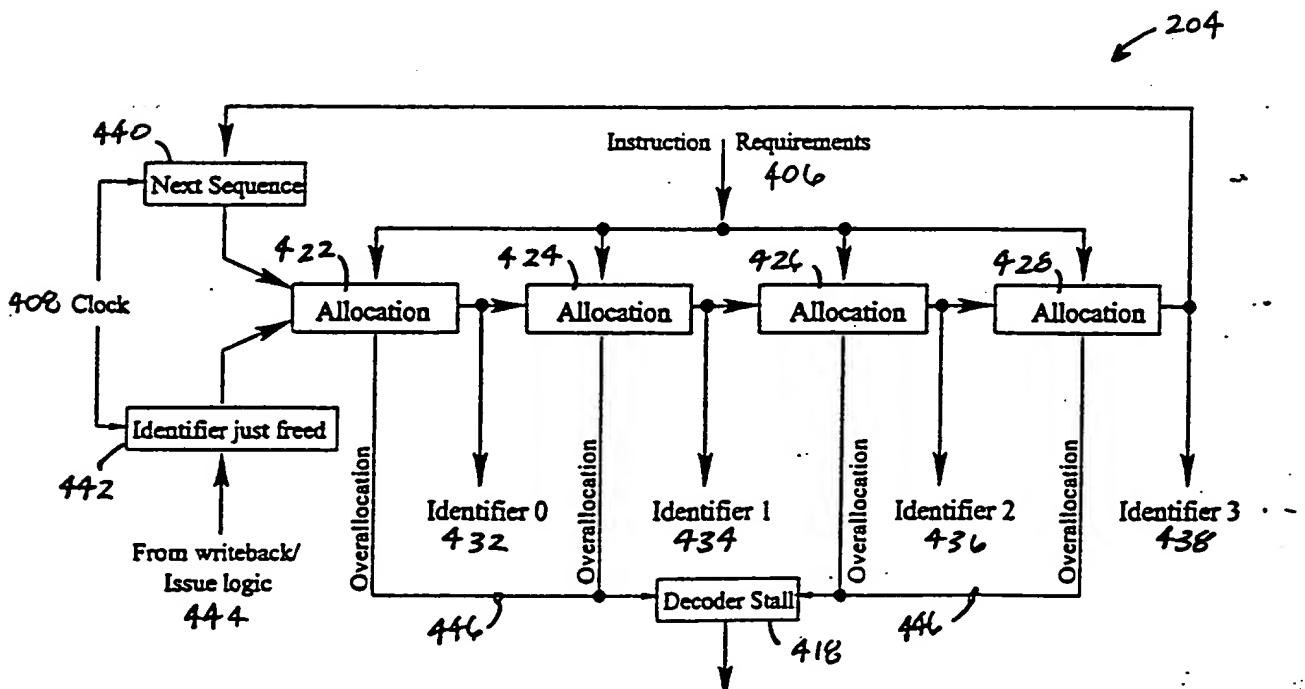


FIG. 4b

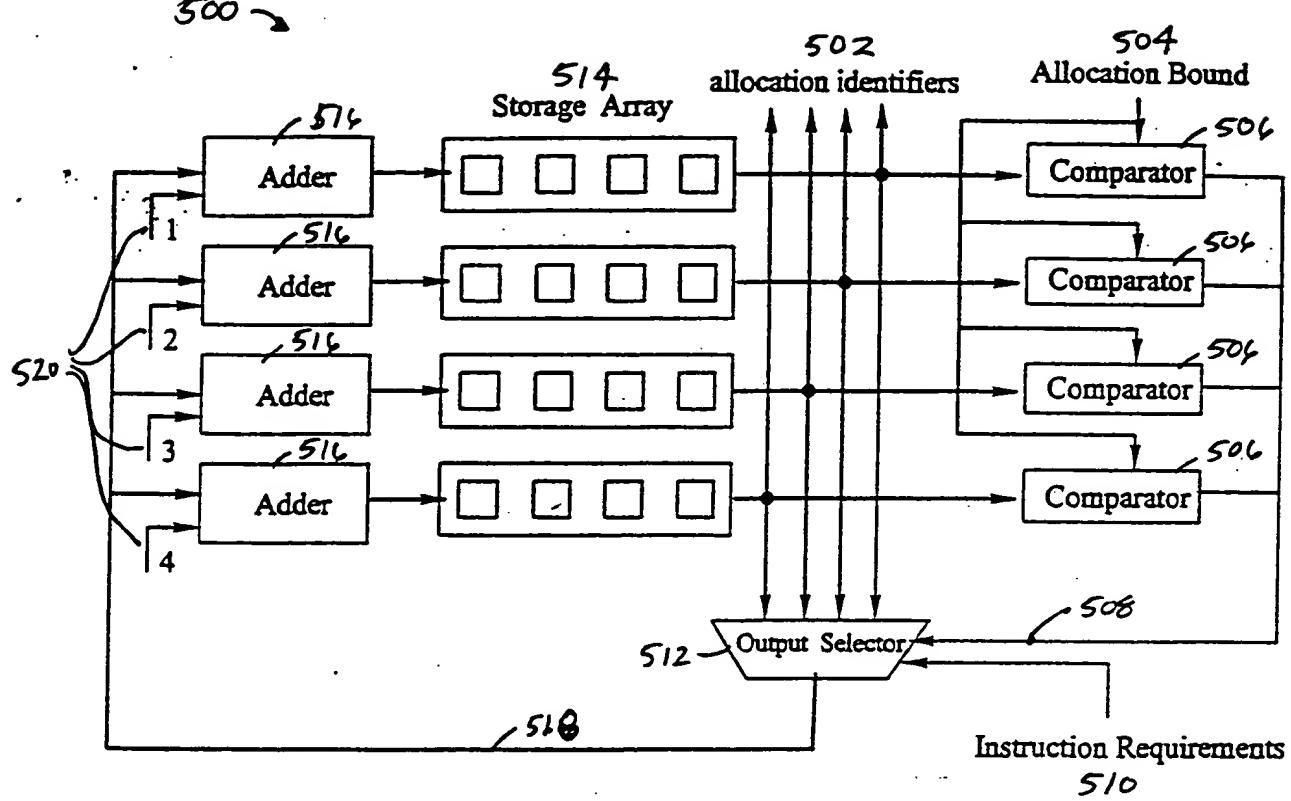


FIG. 5

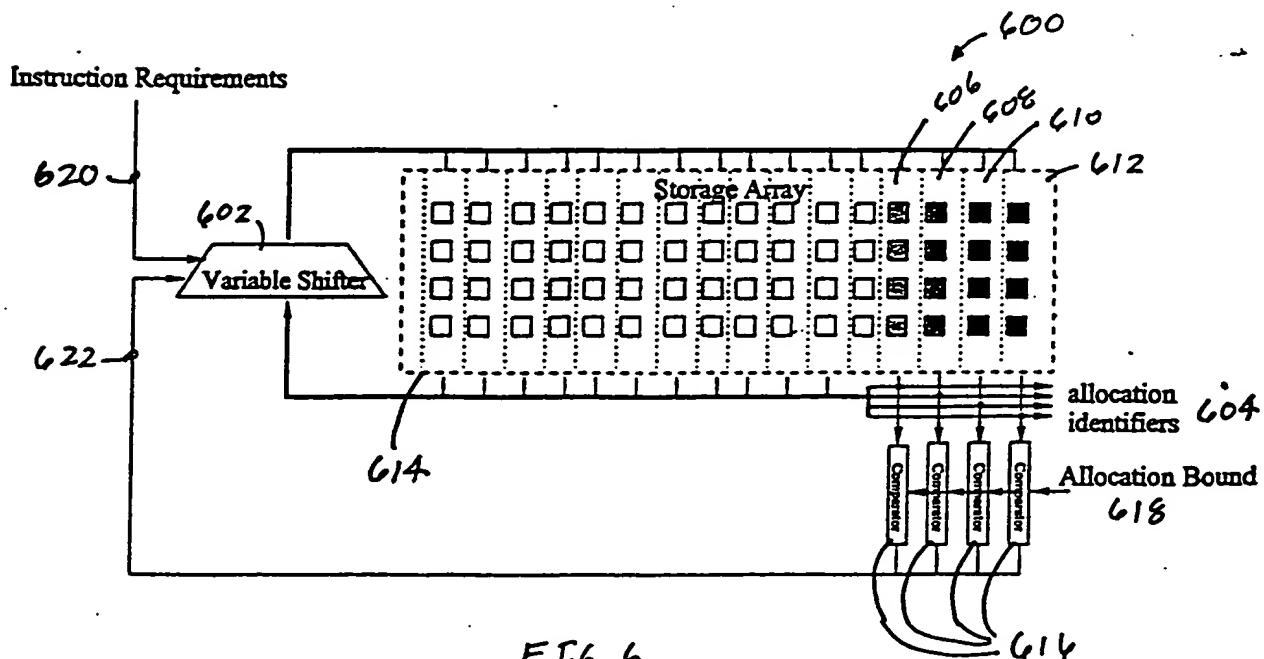


FIG. 6.

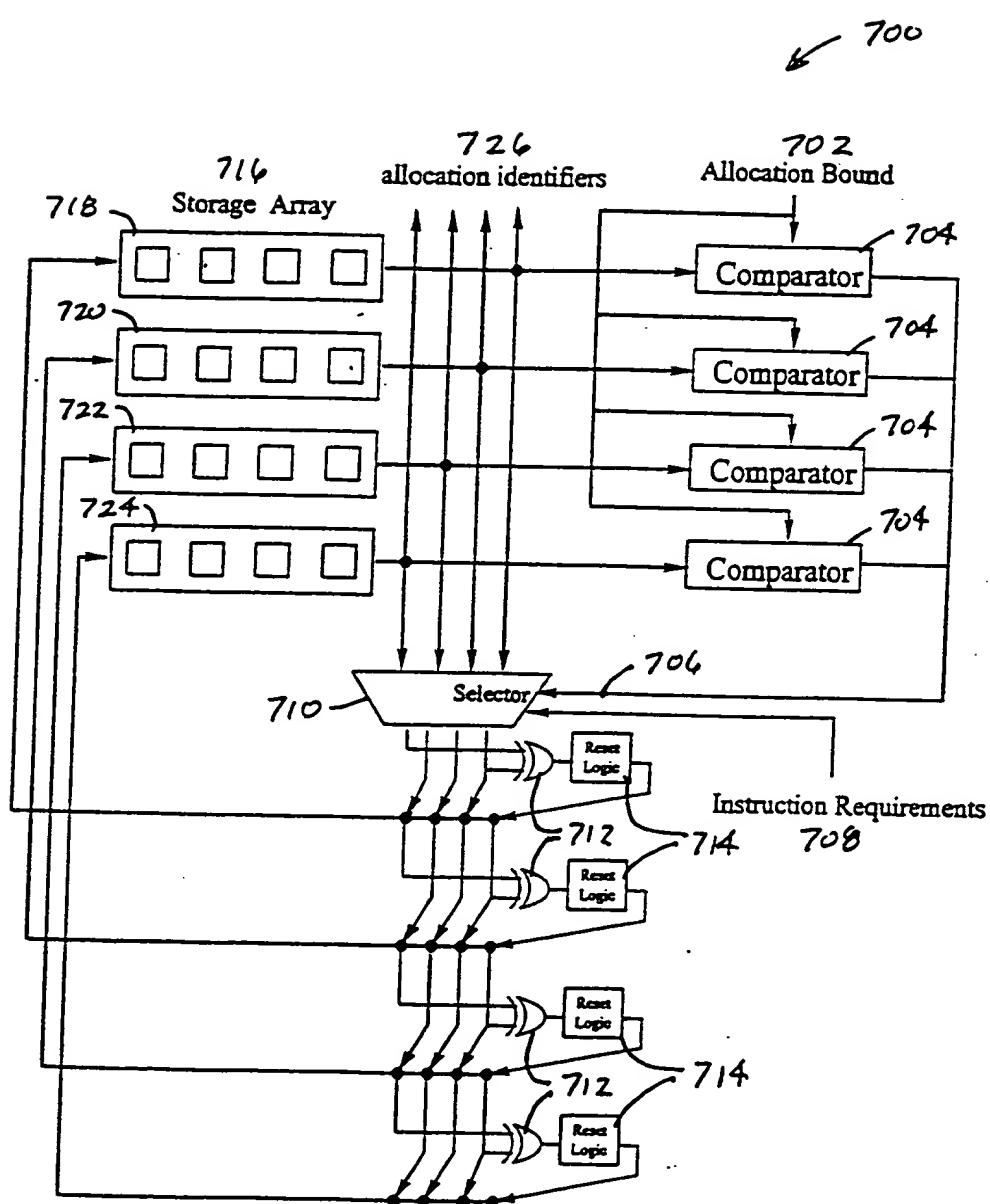


FIG. 7

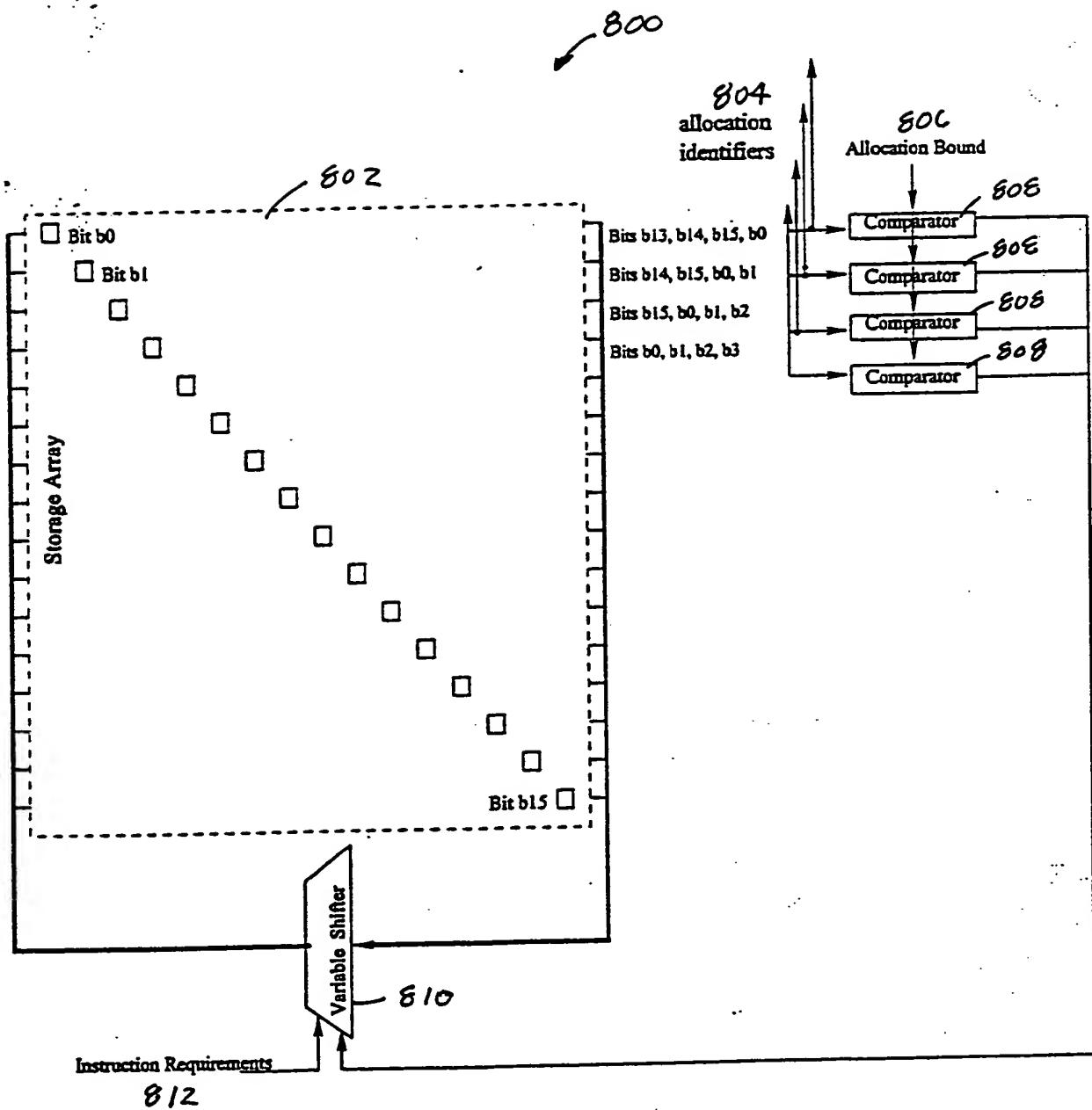


FIG. 8

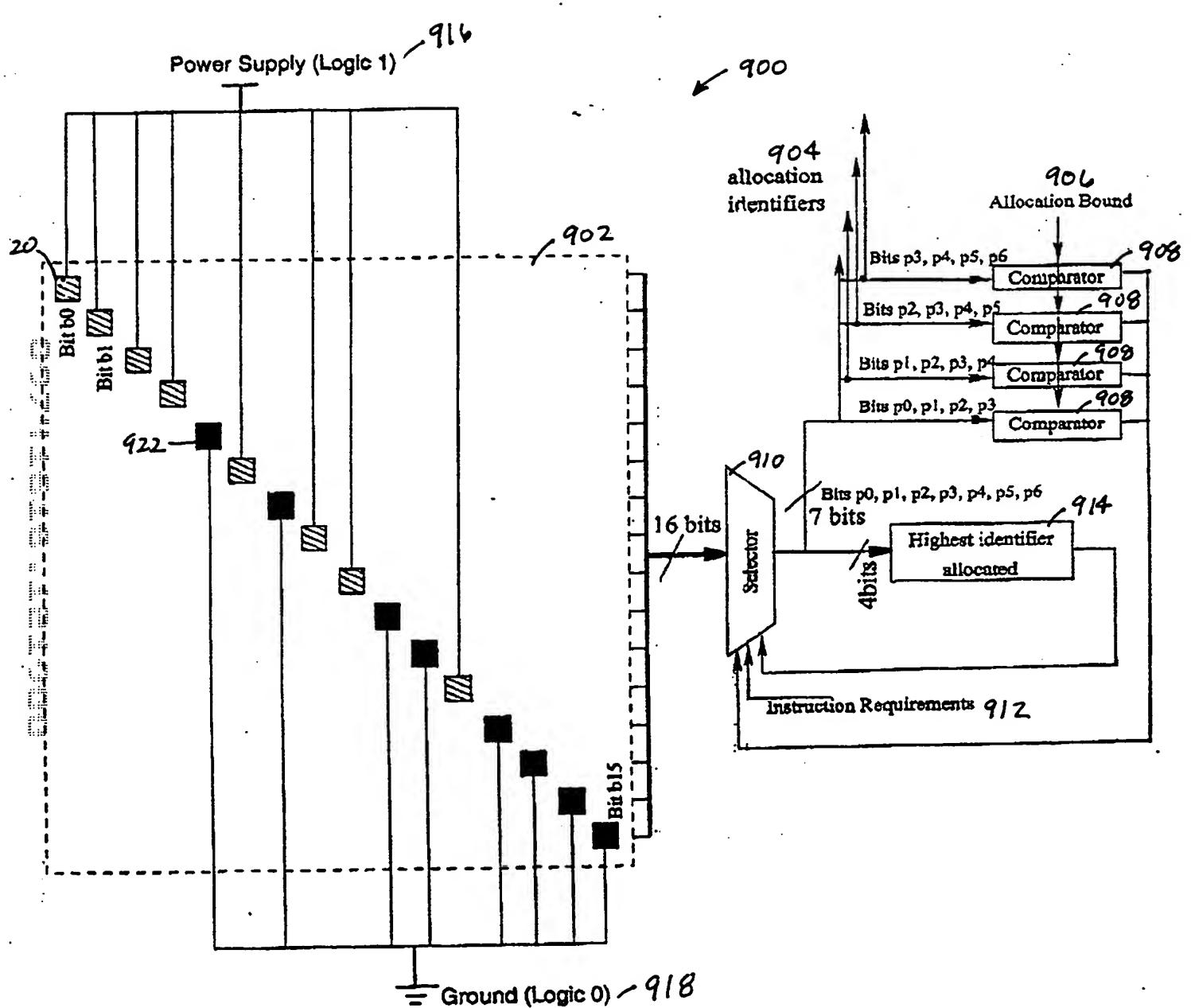


FIG. 9

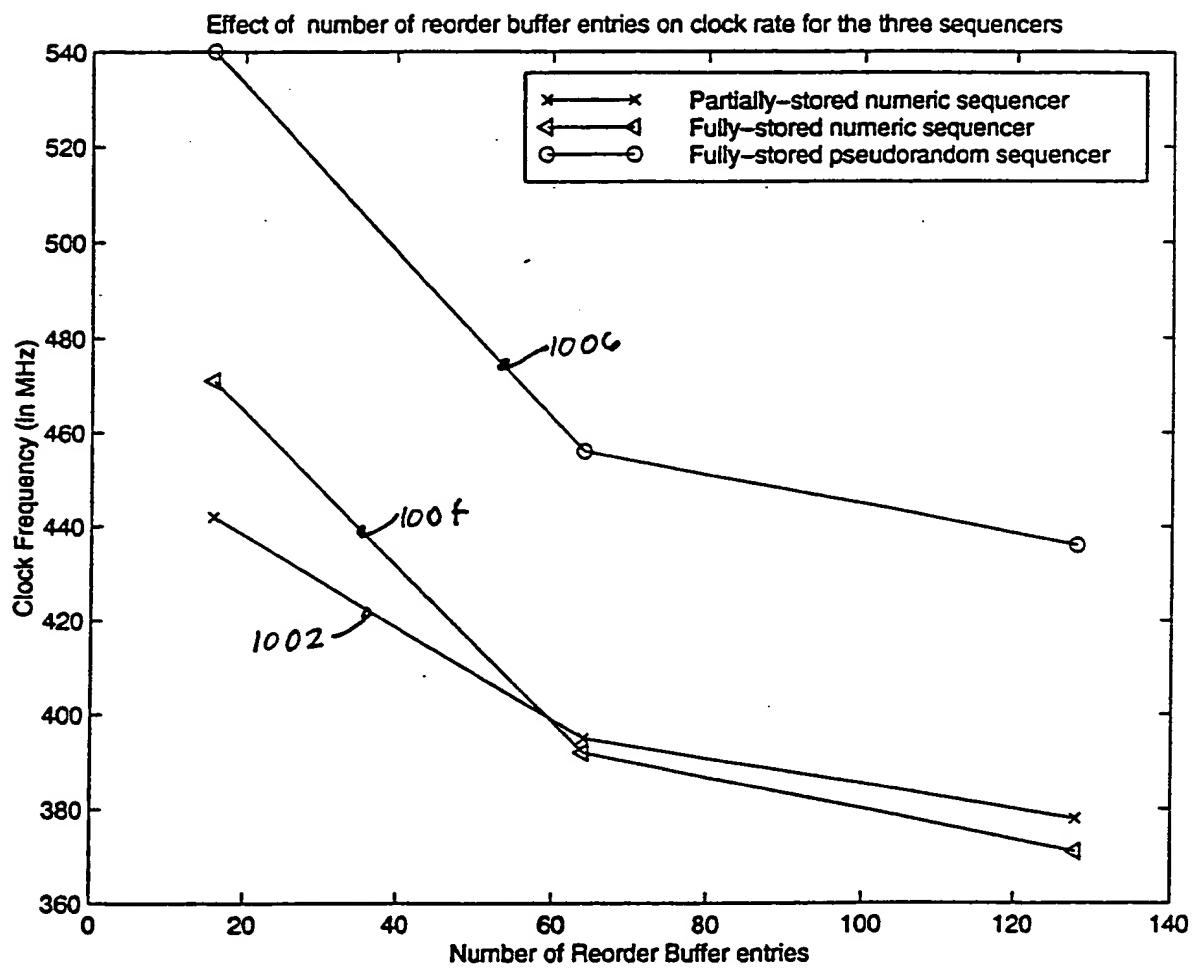


Figure 10: Effect of number of reorder buffer entries on clock rate for the three sequencers